

Instrument Business Department

ARITHMETICAL LOGIC Specification

Version : V1.00



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1 Software Installation

Please install the software as the following steps :

***** Remark: The installation steps for all buses are the same; you can complete installation by following procedures. The below is an example to install SSI bus.

STEP 1. Install Bus Module



STEP 2. Click Install.



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STEP 3. Click Next.



STEP 4. Select **I accept the terms in the license agreement**, and then press **Next**.

1	Special Bus SSI Module - InstallShield Vizard	\mathbf{X}
	License Agreement Please read the following license agreement carefully.	
	LICENSE AGREEMENT	
	IMPORTANT-READ CAREFULLY : This LICENSE AGREEMENT is	
	entered into effect between ZEROPLUS Technology Co., Ltd. (hereinafter	
	"ZEROPLUS") and Customer (Individual or Registered Company).	
	Whereas, ZEROPLUS owns a software product, including computer	
	software as a package product for certain computer products, relevant	
	downloadable software, electronic file and service, known as "ZEROPLUS	~
	I accept the terms in the license agreement	
	○ I do not accept the terms in the license agreement	_
Ir	nstallShield	
	< <u>B</u> ack <u>N</u> ext > Cancel	



STEP 5. Fill in user information in the below dialog box and click **Next**.

🙀 Special Bus SSI Module - InstallShield W	/izard 🛛 🔀
Customer Information Please enter your information.	
User Name:	
Organization: zeroplus	
Install this application for:	
Anyone who uses this computer (all users)	
Only for <u>m</u> e (sunshine)	
InstallShield	ext > Cancel

STEP 6. At first, select **Complete** and then click **Next**.

🛃 Special Bu	s SSI Nodule - InstallShield Vizard 🔀
Setup Type Choose the set	up type that best suits your needs.
Please select a	setup type.
⊙ <u>Complete</u>	All program features will be installed. (Requires the most disk space.)
Cu <u>s</u> tom	Choose which program features you want installed and where they will be installed. Recommended for advanced users.
InstallShield	< <u>B</u> ack <u>N</u> ext > Cancel



STEP 7. Click **Install** to begin the installation.



STEP 8. Click **Finish** to complete the installation.





2 User Interface

Please refer to the below images to select options of setting Arithmetical Logic module

SPECIAL BUS Arithmetic	al Logic SETUP:Bus1	
Configuration Package Regi	ster	
Function Operation		-Pin Assignment
Logic Element Select:	Adder	
Operation Bit Width(N):	2	
G Binary Operation	C BCD Operation	Custom Channel
Operation Formula: A(n)+B($(n) + Cin = Y(n) + C(out) *2^n$	
Output Delay Settings		
🦵 Customize delay time	higher limit 50	ns (lower limit O)
Bus Color		
RESULT	REMAINDER	ERROR
	OK Cancel	Default Help

Custom Channel: Adder

Pi	in Assi	ignment (Ad	der)					
Г	Input					Output -		Carry-in bit
		d A(n)		d B(n)	1	ESOM A(n)	
	A(0)	A0 💌	B(0)	A2 💌		Y(0)	A4 💌	C(in) is Fixed
	A(1)	A1 💌	B(1)	A3 💌		Y(1)	A5 💌	0 🔻
	A(2)	NO CHA	B(2)	NO CHA		Y(2)	NO CHA	
	A(3)	NO CHA	B(3)	NO CHA		Y(3)	NO CHA	
	A(4)	NO CHA	B(4)	NO CHA		Y(4)	NO CHA	Carry-out bit
	A(5)	NO CHA	B(5)	NO CHA		Y(5)	NO CHA	🔲 No Carry-out bit
	A(6)	NO CHA	B(6)	NO CHA		Y(6)	NO CHA	
	A(7)	NO CHA	B(7)	NO CHA		Y(7)	NO CHA	C(out) A7 💌
	🗌 Virtua	al output decodin	ng mode					
					Oł		Cancel	Default



×

Custom Channel: Subtract

Pin Assignment (Subtract)

_ Input —				Output -		-Borrow Input
Minuen	d A(n)	Subtra	hend B(n)	Differe	nce Y(n)	
A(0)	A0 💌	B(0)	A2 🔻	Y(0)	A4 💌	
A(1)	A1 🔻	B(1)	A3 🔻	Y(1)	A5 💌	0 🗸
A(2)	NO CHA	B(2)	NO CHA	Y(2)	NO CHA	C(ip) 46 💌
A(3)	NO CHA	B(3)	NO CHA	Y(3)	NO CHA	
A(4)	NO CHA	B(4)	NO CHA	Y(4)	NO CHA	Borrow Output
A(5)	NO CHA	B(5)	NO CHA	Y(5)	NO CHA	No Borrow Output
A(6)	NO CHA	B(6)	NO CHA	Y(6)	NO CHA	
A(7)	NO CHA	B(7)	NO CHA	Y(7)	NO CHA	C(out) A7 💌
	l output decodir	a mode				
, ,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	, carpar accoun	ig mode	(ОК	Cancel	Default

Custom Channel: Multiplier

Pin	Assi	ignment (Iu	ltiplie	er)						×
I⊓	iput —					-Output -				_
Г	Multiplic	and A(n)	Multipli	cator B(n)		Product	t Y(2n)			۱ ۲
	A(0)	A0 💌	B(0)	A2 💌		Y(0)	A4 🔻	Y(8)	NO CHA	
	A(1)	A1 💌	B(1)	A3 💌		Y(1)	A5 💌	Y(9)	NO CHA	
	A(2)	NO CHA	B(2)	NO CHA		Y(2)	A6 💌	Y(10)	NO CHA	
	A(3)	NO CHA	B(3)	NO CHA		Y(3)	A7 💌	Y(11)	NO CHA	
	A(4)	NO CHA	B(4)	NO CHA		Y(4)	NO CHA	Y(12)	NO CHA	
	A(5)	NO CHA	B(5)	NO CHA		Y(5)	NO CHA	Y(13)	NO CHA	
	A(6)	NO CHA	B(6)	NO CHA		Y(6)	NO CHA	Y(14)	NO CHA	
	A(7)	NO CHA	B(7)	NO CHA		Y(7)	NO CHA 🔻	Y(15)	NO CHA	
Virtual output decoding mode										
				<u> </u>	0	K	Cancel		Default	



Custom Channel: Divider

Pi	n Assi	ignment (Di	vider)							×
Г	Input —				_	Output				_
	Dividen	d A(n)	Divisor	B(n)		Quotie	nt Y(n)	Remaind	der R(n)	
	A(0)	A0 💌	B(0)	A2 🔻		Y(0)	A4 💌	R(0)	A6 💌	
	A(1)	A1 💌	B(1)	A3 🔻		Y(1)	A5 💌	R(1)	A7 💌	
	A(2)	NO CHA	B(2)	NO CHA 👻		Y(2)	NO CHA	R(2)	NO CHA	
	A(3)	NO CHA	B(3)	NO CHA 👻		Y(3)	NO CHA	R(3)	NO CHA	
	A(4)	NO CHA	B(4)	NO CHA 👻		Y(4)	NO CHA	R(4)	NO CHA	
	A(5)	NO CHA	B(5)	NO CHA 👻		Y(5)	NO CHA	R(5)	NO CHA	
	A(6)	NO CHA	B(6)	NO CHA		Y(6)	NO CHA	R(6)	NO CHA	
	A(7)	NO CHA	B(7)	NO CHA		Y(7)	NO CHA	R(7)	NO CHA 🔻	
	Virtua	I output decodin	ig mode				Display the	remainder		
					C	Ж)	Cance	I	Default	

Function Operation settings:

Logic Element Select: User can select the arithmetical element as his/her requirements from the dropdown menu; the options are Adder, Subtract, Multiplier and Divider.

Operation Bit Width Option: The Bit Width can be selected from the dropdown menu; the different options are 2, 4 and 8.

Select the Binary Operation or BCD Operation (The BCD operation is available for the bit width is not less than 4 bit.)

Pin Assignment:

Custom Channel Option: When user selects the different arithmetical elements, the corresponding arithmetical element channel setting will present, and user should set the required channels and the number of the channels.

Output Delay Settings:

Customize the delay time: The default of the delay time higher limit is 50ns and the max. higher limit is 5000ns.

Bus Color

User can customize the color.

Virtual output decoding mode:

User can set the virtual output decoding mode under every channel of Pin Assignment, when it is selected, the output channel is disabled.



Arithmetical Logic Package Dialog Box

SPECIAL BUS Arithmetical Logic SETUP:Bus1	
Configuration Package Register	
Item Color	
RESULT Package Length: 1	
remainder	
🔽 DESCRIBE 🔤	
OK Cancel Default H	lp

In the package part, user can vary the displaying item and the package color and set the package length.

Arithmetical Logic Register Dialog Box

SPECIAL BUS Arithmetical Logi	ic SETUP:Bus1	×
Configuration Package Register		
Congratulation Arithmetical Logi	c decoding function has been activated!	
If you have questions about operat instructions below.Our technical s	ing software please follow the appropriate support team will be happy to answer any	
questions you have.		
>> By phone:	Tel:886-2-66202225	
>> Applications through EMail:	service_2@zeroplus.com.tw	
>> Website:	http://www.zeroplus.com.tw	
Copyright(C) 1997-2008 ZEROPLUS TH	CHNOLOGY CO;LTD	
	OK Cancel Default Help	

There is Zeroplus company information. If you have any questions about software operations, you can contact Zeroplus by Telephone or Email.



3 Operating Instructions

STEP 1. At first, group the unanalyzed channels into **bus1** by pressing the **Right Key** on mouse.



STEP 2. Select **Bus1**, then press **Right key** on mouse to list menu, then press **Bus Property** or **Bus** bar on the toolbar to open **Bus Property** dialog box.





STEP 3. For Special Bus Arithmetical Logic Parameters Configuration, select Special Bus, and then choose **ZEROPLUS Arithmetical Logic MODULE V1.00, next** click **Parameters Configuration** to open **Parameters Configuration** dialog box.

Bus Property	
General Bus Setting	
C GENERAL BUS	Color Config
Special Bus Setting	
SPECIAL BUS	Parameters Config
C ZEROPLUS LA ST MODULE V1.01	~
C ZEROPLUS LA ST7669 MODULE V1.0	1
C ZEROPLUS LA UART MODULE V2.01	
C ZEROPLUS LA USB 1.1 MODULE V1.0	03
C ZEROPLUS LA Digital Logic MODULE	V1.00
C ZEROPLUS LA CCIR656 MODULE V1.	.02
C ZEROPLUS LA PM MODULE V1.02	
C ZEROPLUS LA LCD12864 MODULE VI	1.00(InternalV0.1)
ZEROPLUS LA Arithmetical Logic MOD	DULE V1.00(InternalV0
Use the DsDp	
Other More Module: http://www.zeroplu	s.com.tw
ОК	Cancel Help

STEP 4. Press Register tab to type the serial key number of logic analyzer. Then, press **Register.**

SPECIAL BUS Arithmetical Logic SETU	P:Busl 🔀					
Configuration Package Register The Arithmetical Logic bus decoding function is optional purchased item. Welcome to purchase its serial key to activate this function for your necessary. Enter serial key: If you ordered software or have questions about ordering software please follow the appropriate instructions below. Our sales team will						
respond to your enquiry as soon as possible	e					
>> By phone: Tel:886	3-2-66202225					
>> Applications through EMail: service	e_2@zeroplus.com.tw					
>> Website: http://	/www.zeroplus.com.tw					
Copyright(C) 1997-2008 ZEROPLUS TECHNOLOGY CO;LTD						
Register	Cancel Default Help					



STEP 5. After completing **Register**, come back to the **Bus property** dialog box, and then click the **parameters configuration** to start the Special Bus Digital Logic setup.

Bus Property
General Bus Setting
Color Config
- Special Bus Setting
SPECIAL BUS Parameters Config
C ZEROPLUS LA ST MODULE V1.01
C ZEROPLUS LA ST7669 MODULE V1.01
C ZEROPLUS LA UART MODULE V2.01
C ZEROPLUS LA USB 1.1 MODULE V1.03
C ZEROPLUS LA Digital Logic MODULE V1.00
C ZEROPLUS LA CCIR656 MODULE V1.02
C ZEROPLUS LA PM MODULE V1.02
C ZEROPLUS LA LCD12864 MODULE V1.00(InternalV0.1)
ZEROPLUS LA Arithmetical Logic MODULE V1.UU(InternalVU
Other More Module: http://www.zeroplus.com.tw
OK Cancel Help

STEP 6. Arithmetical operation settings, the useable logic elements are Adder, Subtract, Multiplier and Divider.

SPECIAL BUS Arithmetical Logic SETUP:Bus1	×
Configuration Package Register	
Function Operation Pin Assignment	
Logic Element Select: Adder	
Operation Bit Width(N): 2	
© Binary Operation © BCD Operation Custom Channel	
$\begin{array}{c} \texttt{Operation} \\ \texttt{Formula:} \end{array} A(n) + B(n) + \texttt{Cin} = \texttt{Y}(n) + \texttt{C(out)} * 2 \end{array}^n$	
Cutnut Delay Settings	
Customize delay time higher limit 50 ns (lower limit 0)	
Bus Color	
RESULT REMAINDER ERROR	
OK Cancel Default Help	



STEP 7. The Operation Bit Width can be selected from the three options, there are: 2 bit, 4 bit and 8 bit.

SPECIAL BUS Arithmetical Logic SETUP:Bus1	×
Configuration Package Register	
Function Operation Pin Assignment	
Logic Element Select: Adder 💌	
Operation Bit Width(N): 2	
© Binary Operation C BCD Operation	
Operation Formula: $A(n) + B(n) + Cin = Y(n) + C(out) * 2^n$	
Output Delay Settings	
🔽 Customize delay time higher limit 50 ns (lower limit O)	
Bus Color	
RESULT REMAINDER ERROR	
OK Cancel Default Help	

STEP 8. Select the Binary Operation or BCD Operation (The BCD operation is available for the bit width is not less than 4 bit.)

SPECIAL BUS Arithmetical Logic SETUP:Bus1	×
Configuration Package Register	
Function Operation Logic Element Select: Adder Operation Bit Width (W): Binary Operation Operation	
Formula: $A(n) + B(n) + Cin = Y(n) + C(out) * 2^n$	
Output Delay Settings Customize delay time higher limit 50 ns (lower limit O)	
Bus Color	
RESULT REMAINDER ERROR	
OK Cancel Default Help	



STEP 9. When user selects the different arithmetical elements, the corresponding arithmetical element channel setting will present, and user should set the required channels and the number of the channels.

SPECIAL BUS Arithmetic	al Logic SETUP:Bus1	
Configuration Package Regi	ster	
Function Operation Logic Element Select:	Adder	Pin Assignment
Operation Bit Width(N):	4	
G Binary Operation	C BCD Operation	Custom Channel
Operation Formula: A(n)+B($(n) + Cin = Y(n) + C(out) *2^{n}$	
Output Delay Settings		
🦵 Customize delay time	higher limit 50	ns (lower limit O)
Bus Color		
RESULT	REMAINDER	ERROR
····	····	
	OK Cancel	Default Help

STEP 10. Custom Channel: Adder

Pin Assign	ient (Adde	r)					
_ Input					Output -		Carry-in bit
Augend A(p)		Addeno	l B(n)		SUM Y(r	ו)	
A(0) A0		B(0)	A2 🔻		Y(0)	A4 💌	C(in) is Fixed
A(1) A1	-	B(1)	A3 💌		Y(1)	A5 💌	0 🗸
A(2)	CHA	B(2)	NO CHA		Y(2)	NO CHA	
A(3)	CHA	B(3)	NO CHA		Y(3)	NO CHA	
A(4) NO	CHA	B(4)	NO CHA		Y(4)	NO CHA	Carry-out bit
A(5) NO	CHA	B(5)	NO CHA		Y(5)	NO CHA	🔲 No Carry-out bit
X(6) NO	CHA 🚽	B(6)	NO CHA 👻		Y(6)	NO CHA	
A(7)	CHA 👻	B(7)	NO CHA		Y(7)	NO CHA	C(out) 17 💌
🔲 Virtual outp	out decoding i	mode					
				OK		Cancel	Default



STEP 11. Custom Channel: Subtract

Pi	n Ass	ignment (Su	btract))				\mathbf{X}
Г	Input					Output		Borrow Input
	Minuen	d A(n)	Subtra	ahend B(n)		Differer	nce Y(n)	of the New York
	A(0)	A0 🔻	B(0)	A2 💌		Y(0)	A4 🔻	
	A(1)	A1 💌	B(1)	A3 🔻		Y(1)	A5 💌	
	A(2)	NO CHA	B(2)	NO CHA		Y(2)	NO CHA	(in) 06
/	A(3)	NO CHA	B(3)	NO CHA		Y(3)	NO CHA	
	A(4)	NO CHA	B(4)	NO CHA		Y(4)	NO CHA	Borrow Output
\mathbf{N}	A(5)	NO CHA	B(5)	NO CHA		Y(5)	NO CHA	No Borrow Output
	A(6)	NO CHA	B(6)	NO CHA		Y(6)	NO CHA	
	A(7)	NO CHA	B(7)	NO CHA		Y(7)	NO CHA	C(out) A7 💌
L					<u> </u>			
	Virtua	al output accodin	g mode					
					C	Ж	Cancel	Default

STEP 12. Custom Channel: Multiplier

P	in Assi	ignment (Iu	ltiplie	er)						×
	Input					-Output -				
	Multiplic	and A(n)	Multipl	icator B(n)		Product	: Y(2n)			
	A (0)	A0 🔻	B(0)	A2 💌		Y(0)	A4 🔻	Y(8)	NO CNC 👻	
	A(1)	A1 💌	B(1)	A3 💌		Y(1)	A5 💌	Y(9)	NO CHA	
	A(2)	NO CHA	B(2)	NO CHA		Y(2)	A6 💌	Y(10)	NO CHA	
	A(3)	NO CHA	B(3)	NO CHA		Y(3)	A7 💌	Y(11)	NO CHA	
N	A(4)	NO CHA	B(4)	NO CHA		Y(4)	NO CHA	Y(12)	NO CHA	
	A(5)	NO CHA	B(5)	NO CHA		Y(5)	NO CHA	Y(13)	NO CHA Y	
	A(6)	NO CHA	B(6)	NO CHA		Y(6)	NO CHA	Y(14)		
	A(7)	NO CHA	B(7)	NO CHA		Y(7)	NO CHA 🔻	Y(15)	NO CHA 👻	
	🔲 Virtua	l output decodin	ig mode							
				(0	Ж	Cancel		Default	



STEP 13. Custom Channel: Divider

Pi	n Ass	ignment (Di	vider)						×
Г	Input —				 -Output				
	Dividen	id A(p)	Divisor	B(n)	Quotie	ent Y(n)	Remain	der P(n)	
	A(0)	A0 🔻	B(0)	A2 💌	Y(0)	A4 💌	R(0)	A6]
	A(1)	A1 💌	B(1)	A3 💌	Y(1)	A5 💌	R(1)	A7 💌	
	A(2)	NO CHA	B(2)	NO CHA 👻	Y(2)	NO CHA	R(2)	NO CHA]
(A(3)	NO CHA	B(3)	NO CHA	Y(3)	NO CHA	R(3)	NO CHA	
	A(4)	NO CHA	B(4)	NO CHA	Y(4)	NO CHA 🔻	R(4)	NO CHA]
	A(5)	NO CHA 💌	B(5)	NO CHA	Y(5)	NO CHA 🔻	R(5)	NO CHA]/[
	A(6)	NO CHA	B(6)	NO CHA	Y(6)	NO CHA	R(6)	NO CHA	1
	A(7)	NO CHA 🔽	B(7)	NO CHA	Y(7)	NO CHA 🔻	R(7)	NO CHA]
	🗌 Virtua	al output decodin	ig mode			Display the	e remainder	r	
					Ж	Cance		Default	

STEP 14. Customize the delay time.

SPECIAL BUS Arithmetical Logic SETUP:Bus1	×
Configuration Package Register	
Function Operation Logic Element Select: Adder 💌	
Operation Bit Width(N): © Binary Operation © BCD Operation Custom Channel	
Operation Formula: A (n) +B (n) +Cin=Y (n) +C (out) *2 ⁿ	
Customize delay time higher limit 50 ns (lower limit O)	
Bus Color	
RESULT REMAINDER ERROR	
OK Cancel Default Help	



STEP 15. Set the color of the bus

SPECIAL BUS Arithmetic	al Logic SETUP:Bus1	
Configuration Package Reg	ister	
Function Operation Logic Element Select: Operation Bit Width(N): Operation	Adder	Pin Assignment Custom Channel
Formula: A(n)+B	$(n) + Cin=Y(n) + C(out) *2^{n}$	
Output Delay Settings	higher limit 50	ns (lower limit O)
Bus Color RESULT	REMAINDER	ERROR
	OK Cancel	DefaultHelp

STEP 16. Following pictures show the completion of the Bus decoding and package list. The conditions are set as either edge, the Memory depth is 2K, Sampling frequency is 25MHz

Bus Decoding												
Seroplus LAP-32128U-A(S/N:000000-0000) - [LaDoc1]												
🐔 <u>F</u> ile B <u>u</u> s/Signal T <u>r</u> igger	Run/Stop Data Tools Mindow Help -	. a ×										
🗅 😂 🖪 🎒 🔍 🕰 辩	🐢 🐺 📲 📓 🕨 🕪 🔲 🚺 2K 📲 🗤 25MHz 🕽 🚥 🎋 50% 💌 🚸 Page 1 🔍 C	ount [
🚯 🚯 📾 📟 🧊	🔌 🙀 🖑 🎆 📓 🗸 🗯 4.723ns 🗸 🦞 😹 🛃 🛃 🛃 🖬 🗛 🗛 🖓 🖬 🗛 🖓 🖬 🗛 🖓 👘 🖓 🖓 🖓 🖓	- Tri										
Font Size 9 V												
Scale: 4. 723ns	Display Pos:-006.836us A Pos:-600ns ▼ A - T = 600ns ▼ A - B = 1.2us ▼											
Total:81.92us	Trigger Pos:Ons B Pos:600ns V B - T = 600ns V Compr-Rate:No											
Bus/Signal Trigger	Enable -005. 931us-005. 907us-005. 884us-006. 86us-006. 836us-006. 813us-006. 789us-006. 766us-006. 742us-006. 7	18u										
Busl (Arit)	RESULT : 0X1	SULT										
DA DA												
🖌 A1 AJ												
/ BO A2												
B1 A3												
¥ YO A4		~										



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Package List

🚳 zero	OPLUS LAP	-32128U-	A (S/N: 000	0000-0000) - [LaDoc1]						
💭 <u>F</u> ile	e B <u>u</u> s/Signa	l T <u>r</u> igger	Run/ <u>S</u> top <u>I</u>	<u>)</u> ata <u>T</u> ools	<u>W</u> indow <u>H</u> elp					- 8 ×	
🗋 🖻		🔍 🏹 🖗	ψ [®] Υ _T γ [™]	i 🔟 🕨	▶ ■ →ii4 2	K <u>▼</u> I∰I nu	25MHz 💌	<u>™</u> ¥¥ 50%	🔻 📣 Page	1	
1	§ 🔝 🛤		k 📓 🖑		🖌 👗 🖊 4.677ns		AN BAR BAR BAR BAR	👪 le 🔿 🛛 👸	🛾 📴 🔖 🛛 Hei	ght 4 1	
Trigger Delay 40ns Font Size 9 🗸											
Scale:4. Total:81	. 677ns 1. 92us	Di: Tr:	splay Pos:-O igger Pos:Or)07.074us as	A Pos:-600ns B Pos:600ns	✓ A B	A - T = 600ns ▼ 3 - T = 600ns ▼	A - B Compr	= 1.2us 💌 -Rate:No		
Bus/Si;	gnal	Trigger	Enable	-007.1	67u-007. 144u=00	7.12u s -007.097u-	-007. 074u=007. 0	5us-007.027u-007.	003u=006.98u=		
	- <mark>Busl</mark> (Arit)	ł	-	охо	RESULT : OXO	RESULT : OX1	RESULT : OX1	RESULT : OX1	RESULT : OX1	RES	
	🧪 🗚 🕹	x									
	🖌 🖌 A.										
	🥖 BO A2	2									
	🧭 B1 A.									~	
<				<		0				>	
× Setti	ng Flas	h Expor	t Synch F	Parameter							
Package # Name		TimeSta									
Pa	Bus I (Arithmetical Logic)		-040.32								
	2	Bus1(Arithmetical Logic)		-040.88							
Pa	ckage #	Name		TimeSta	mp RESULT						
	3	Bus1(Arithmetical Logic)			-040.84	us OXO					
Pa	ckage # 4	Name Bus1(Arithmetical Logic)			TimeSta -040.8i	np RESULT					
	- <u> </u>		Minimodo	arcogroj						•	
Ready								End!	DEMO		