Instrument Business Department

ARITHMETICAL LOGIC Specification

Version：V1.00

Content

FM-E009A

1 Software Installation

Please install the software as the following steps ：

※ Remark：The installation steps for all buses are the same; you can complete installation by following procedures. The below is an example to install SSI bus.
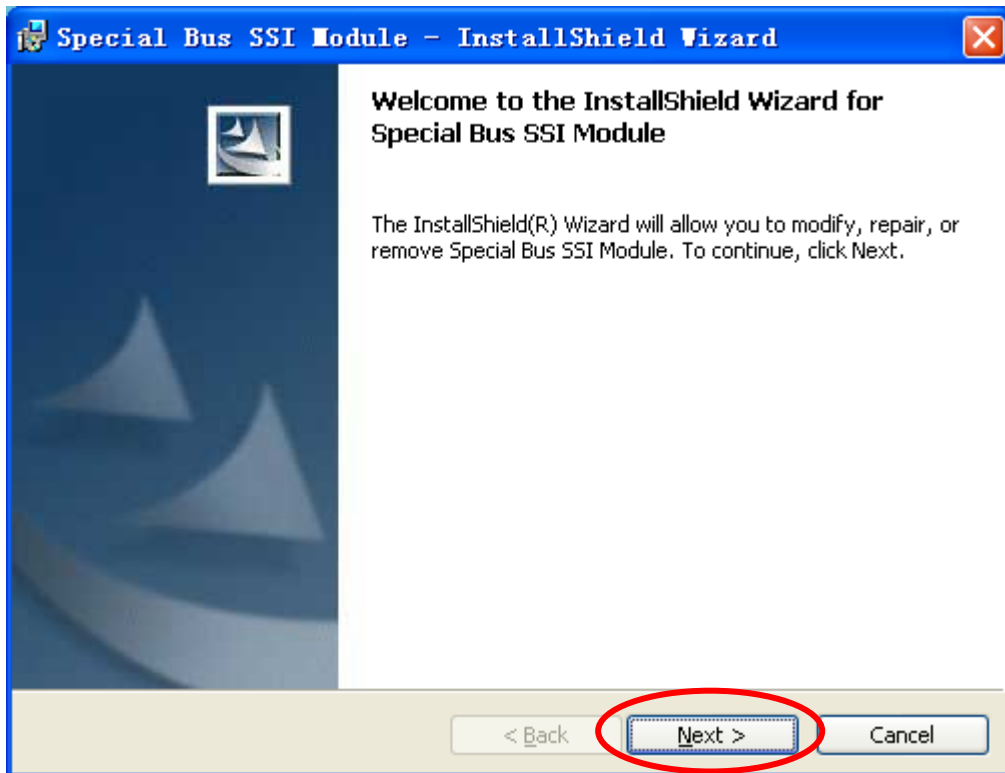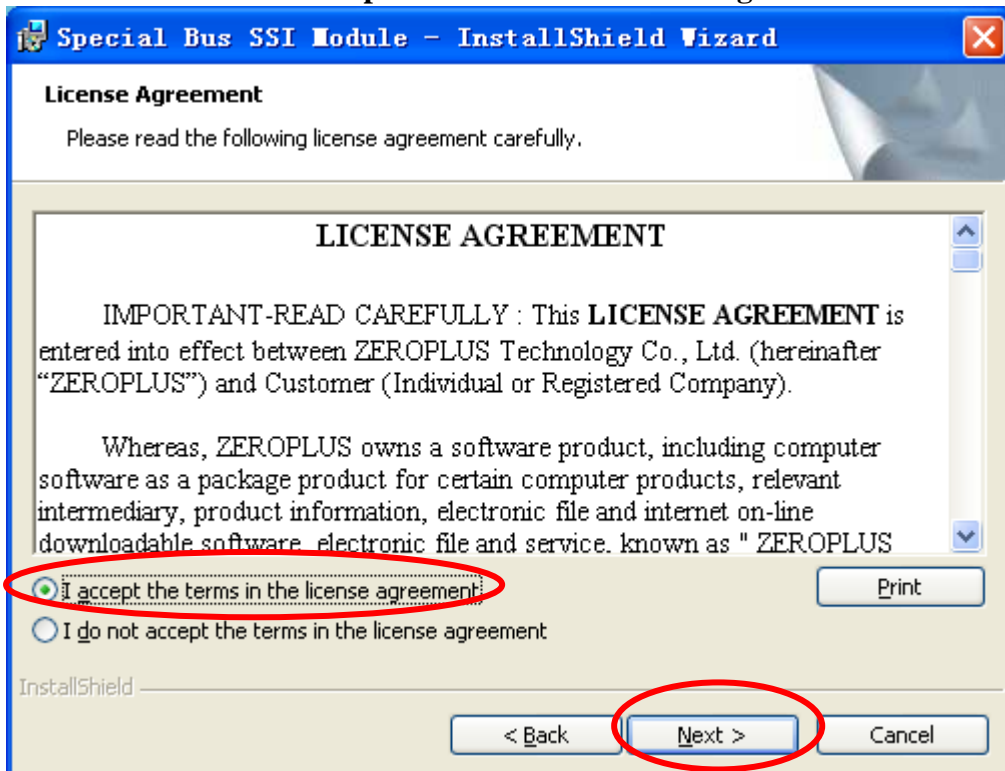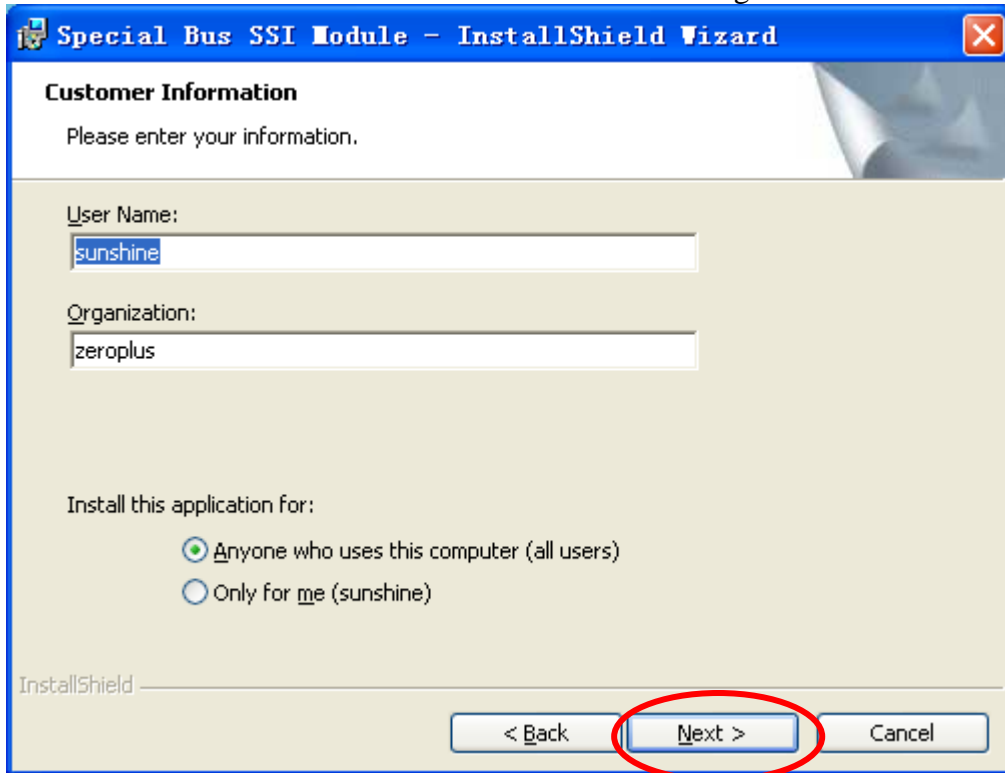
STEP 1.   Install Bus Module

STEP 2.   Click **Install.**

FM-E009A

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ZEROPLUS TECHNOLOGY CO;LTD

3F., No.123,Jian 8 Rd,, Chung Ho City, Taipei
TEL：886-2-66202225
FAX：886-2-66202226
http://www.zeroplus.com.tw
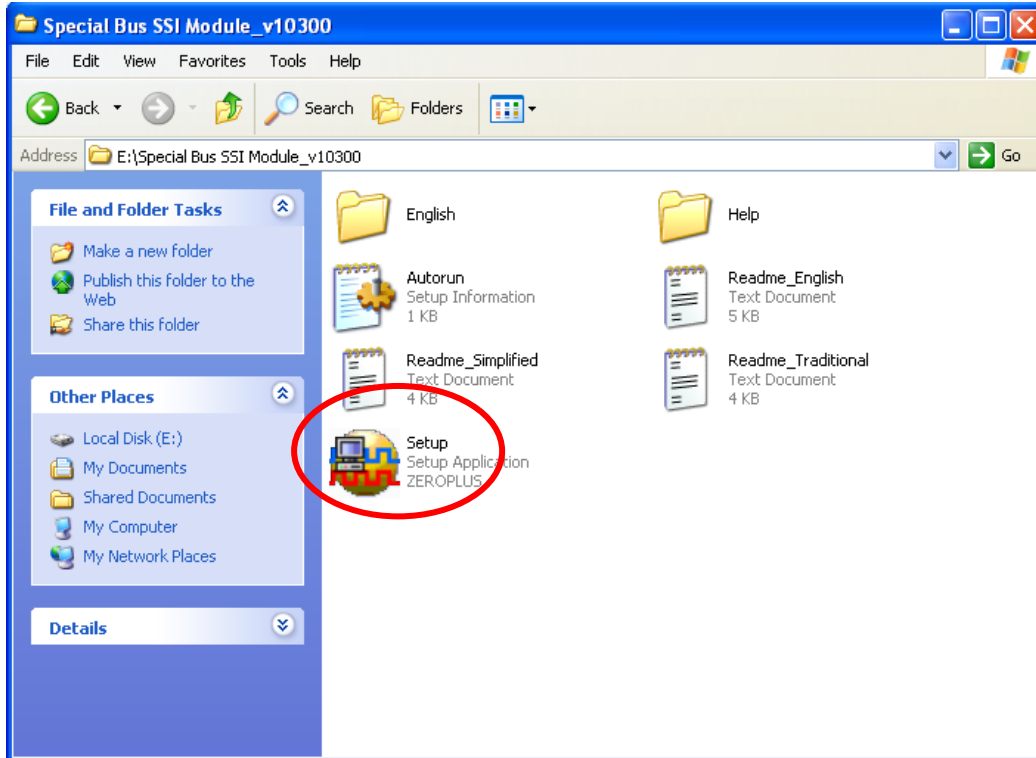
STEP 3.   Click **Next.**

STEP 4.   Select **I accept the terms in the license agreement**，and then press **Next**.

FM-E009A

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TEL：886-2-66202225
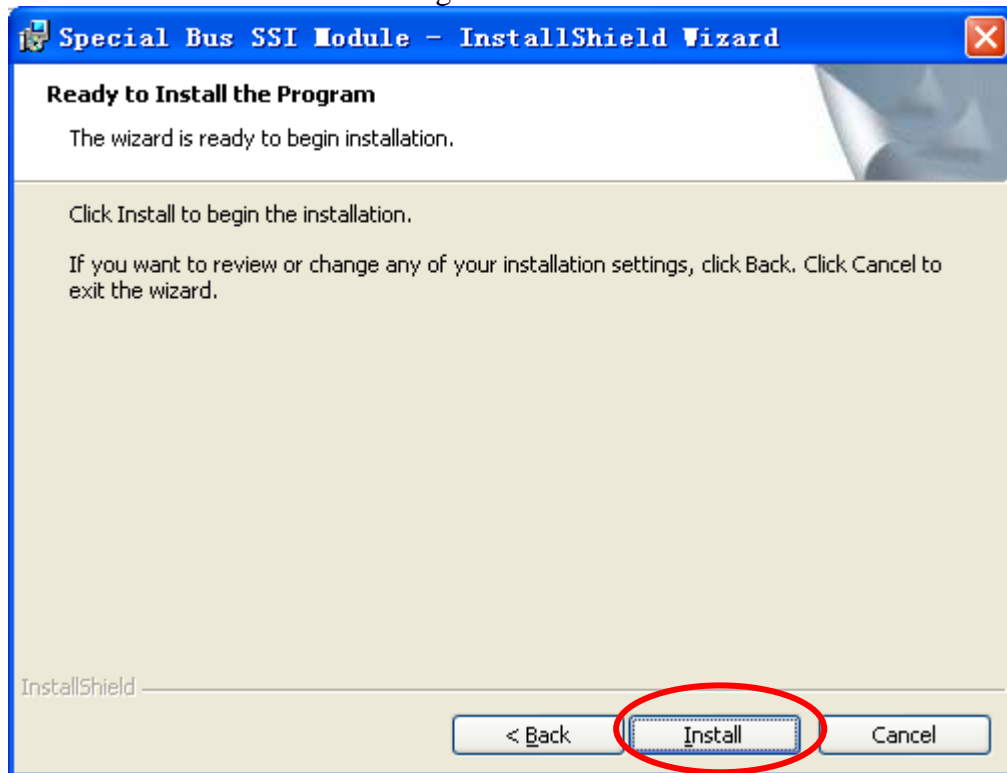FAX：886-2-66202226
http://www.zeroplus.com.tw

STEP 5. Fill in user information in the below dialog box and click **Next**.
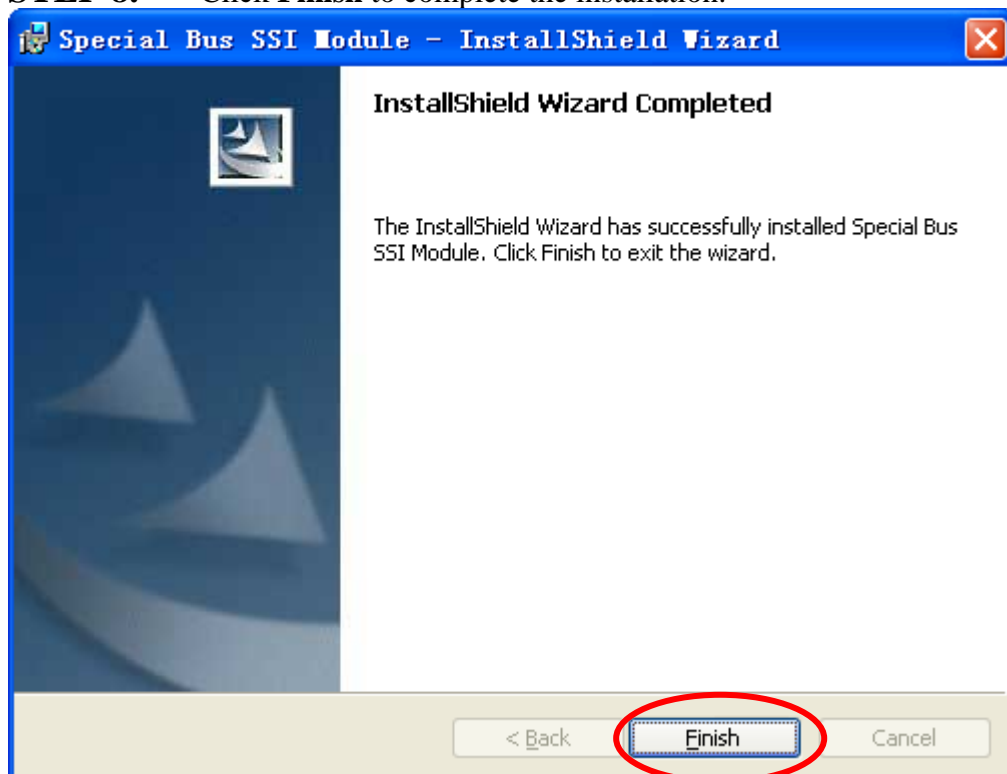

STEP 6. At first, select **Complete** and then click **Next.**

FM-E009A
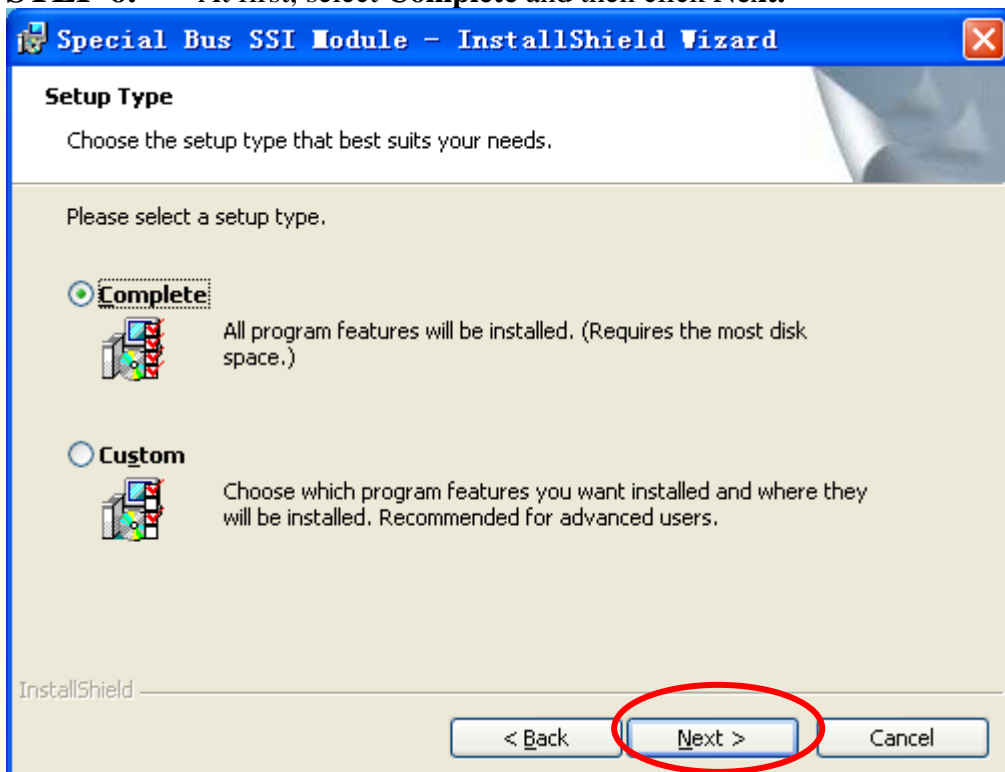
STEP 7. Click **Install** to begin the installation.
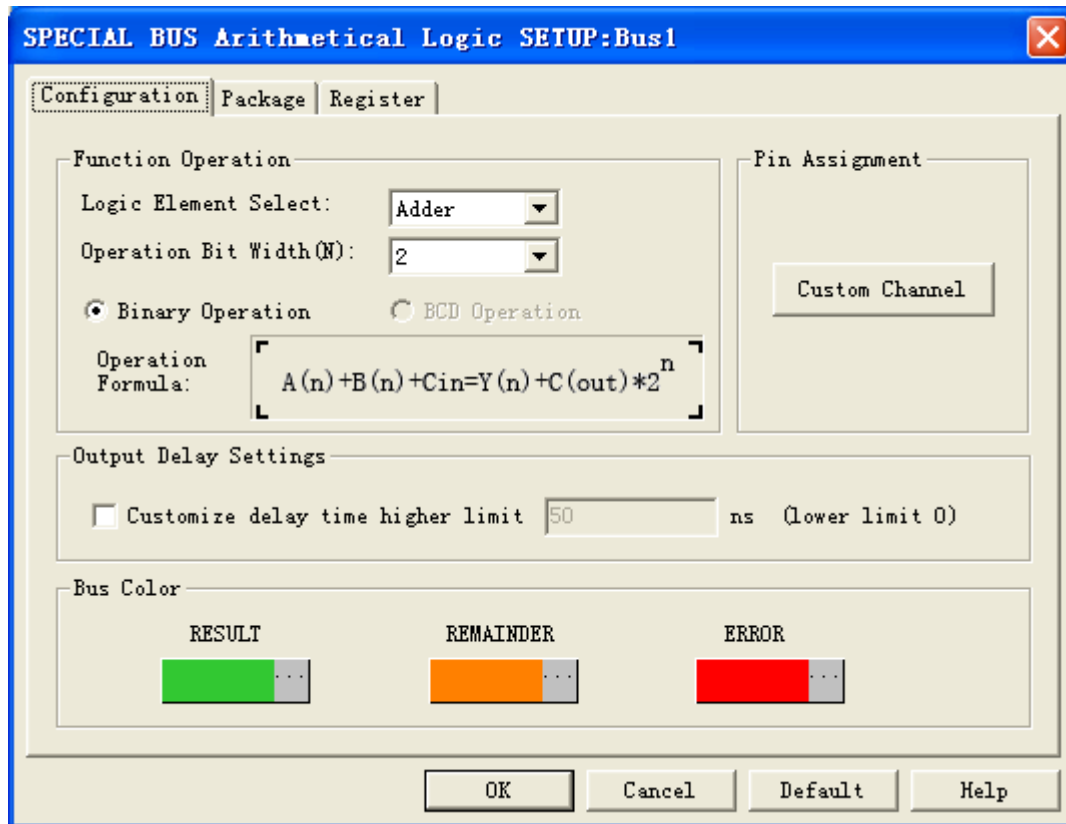

STEP 8. Click **Finish** to complete the installation.
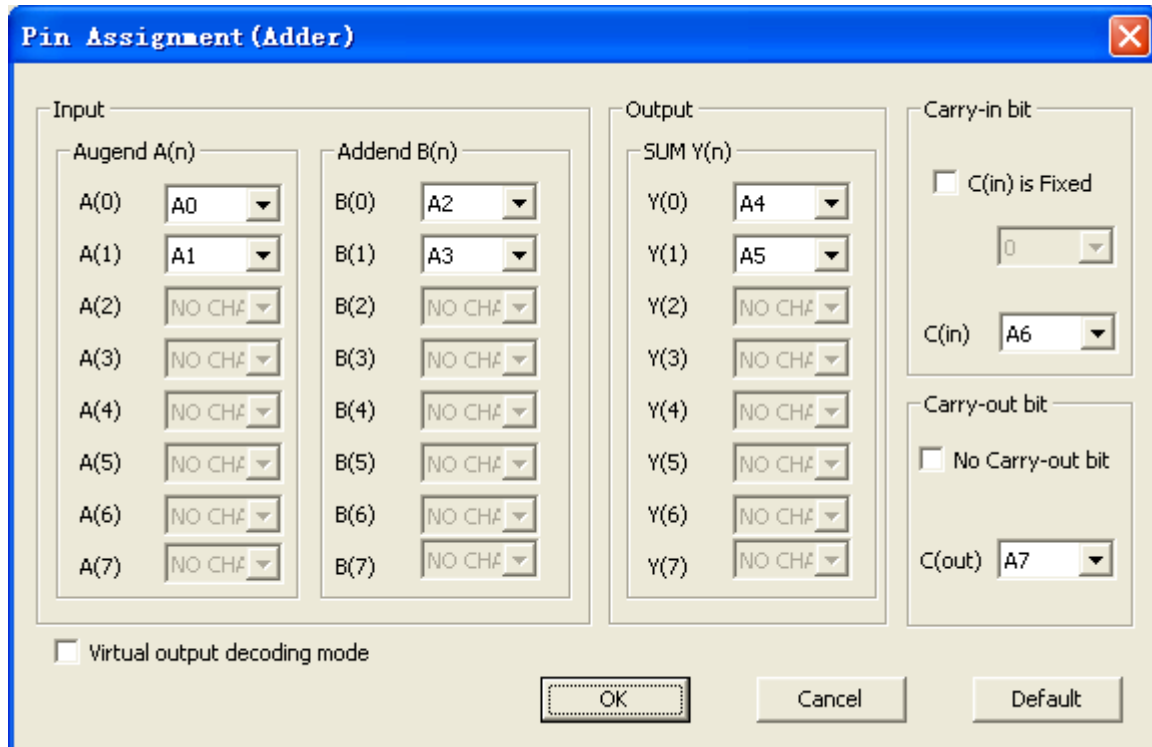
FM-E009A

2 User Interface

Please refer to the below images to select options of setting Arithmetical Logic module

Custom Channel: Adder

FM-E009A

Custom Channel: Subtract

Custom Channel: Multiplier

FM-E009A

Custom Channel: Divider

Function Operation settings:
Logic Element Select: User can select the arithmetical element as his/her requirements from the dropdown menu; the options are Adder, Subtract, Multiplier and Divider.
Operation Bit Width Option: The Bit Width can be selected from the dropdown menu; the different options are 2, 4 and 8.
Select the Binary Operation or BCD Operation (The BCD operation is available for the bit width is not less than 4 bit.)

Pin Assignment:

Custom Channel Option: When user selects the different arithmetical elements, the corresponding arithmetical element channel setting will present, and user should set the required channels and the number of the channels.

Output Delay Settings:

Customize the delay time: The default of the delay time higher limit is 50ns and the max. higher limit is 5000ns.
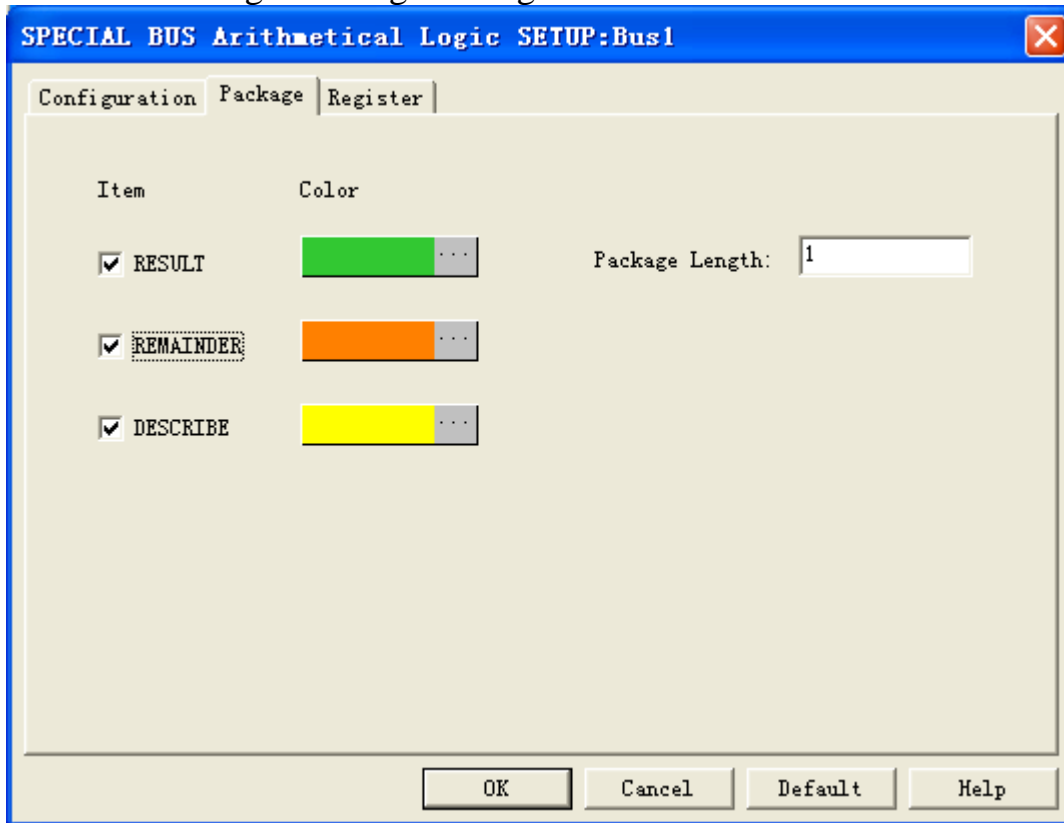
Bus Color
User can customize the color.
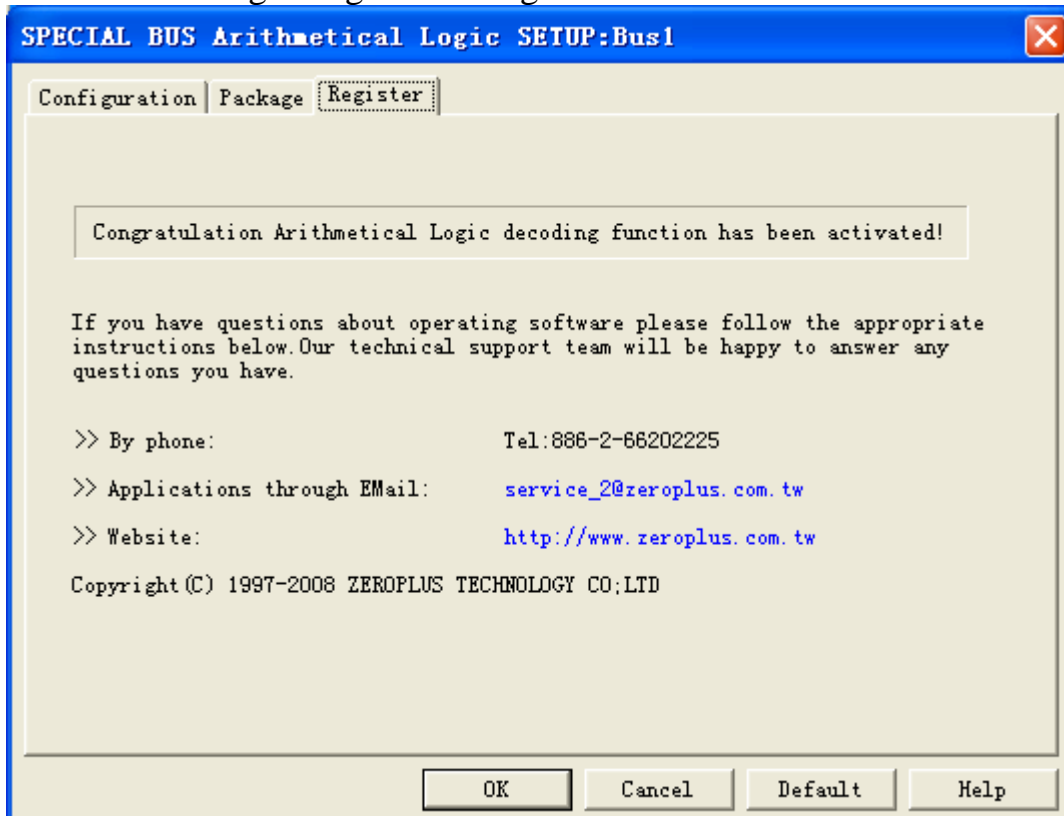
Virtual output decoding mode:
User can set the virtual output decoding mode under every channel of Pin Assignment, when it is selected, the output channel is disabled.

FM-E009A

Arithmetical Logic Package Dialog Box

In the package part, user can vary the displaying item and the package color and set the package length.
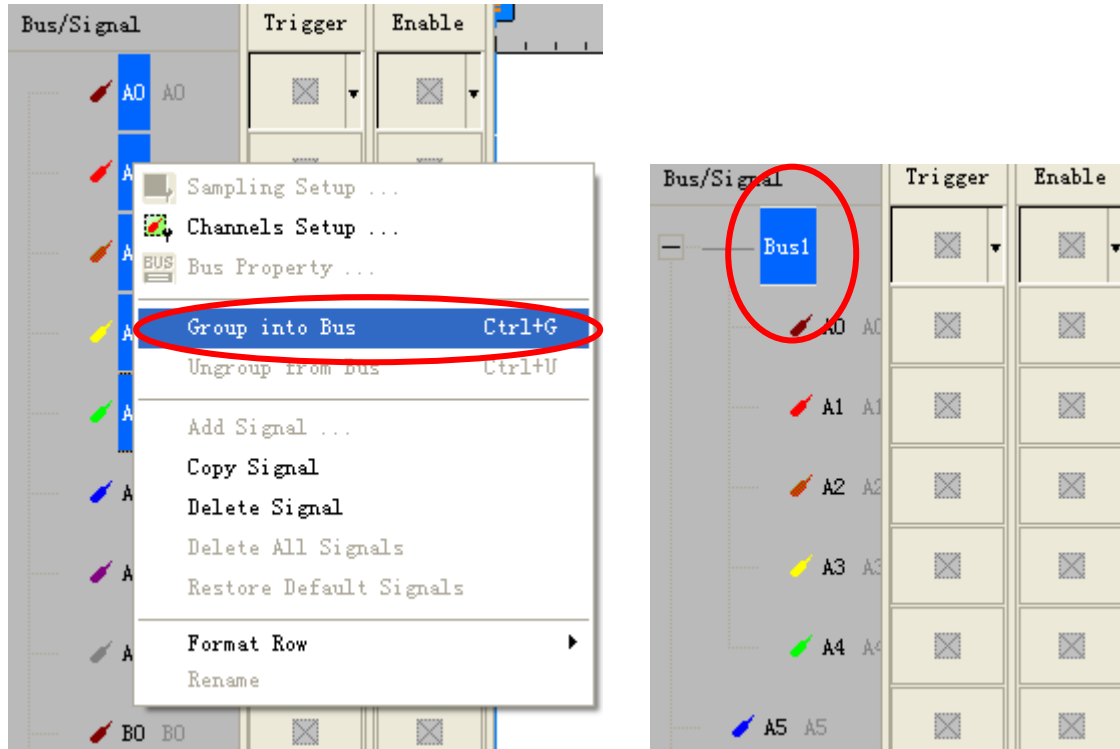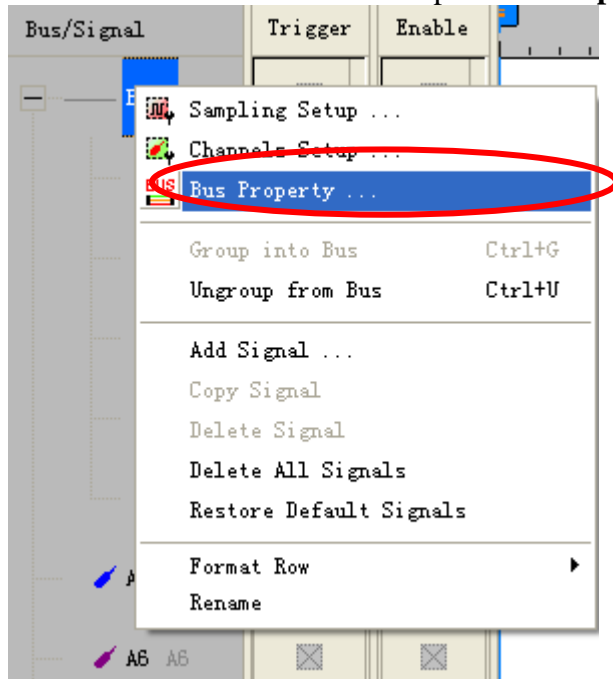
Arithmetical Logic Register Dialog Box

There is Zeroplus company information. If you have any questions about software operations, you can contact Zeroplus by Telephone or Email.

FM-E009A

3 Operating Instructions

STEP 1.　At first, group the unanalyzed channels into **bus1** by pressing the **Right Key** on mouse.


STEP 2.　Select **Bus1**, then press **Right key** on mouse to list menu, then press **Bus Property** or **Bus** bar on the toolbar to open **Bus Property** dialog box.


11/19

FM-E009A

STEP 3. For Special Bus Arithmetical Logic Parameters Configuration, select Special Bus, and then choose **ZEROPLUS Arithmetical Logic MODULE V1.00, next** click **Parameters Configuration** to open **Parameters Configuration** dialog box.
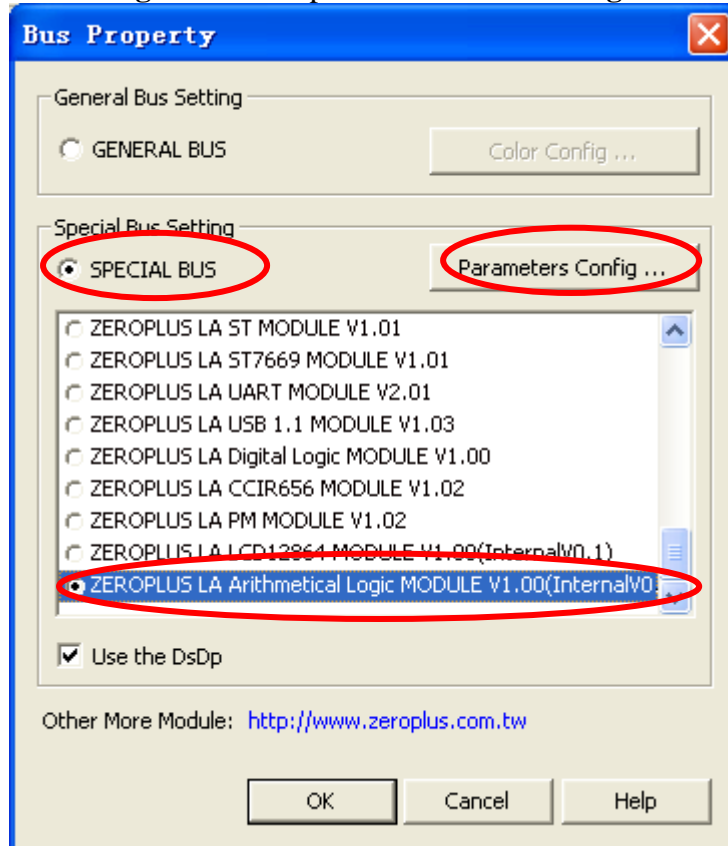

**STEP 4.** Press Register tab to type the serial key number of logic analyzer. Then, press **Register.**

FM-E009A
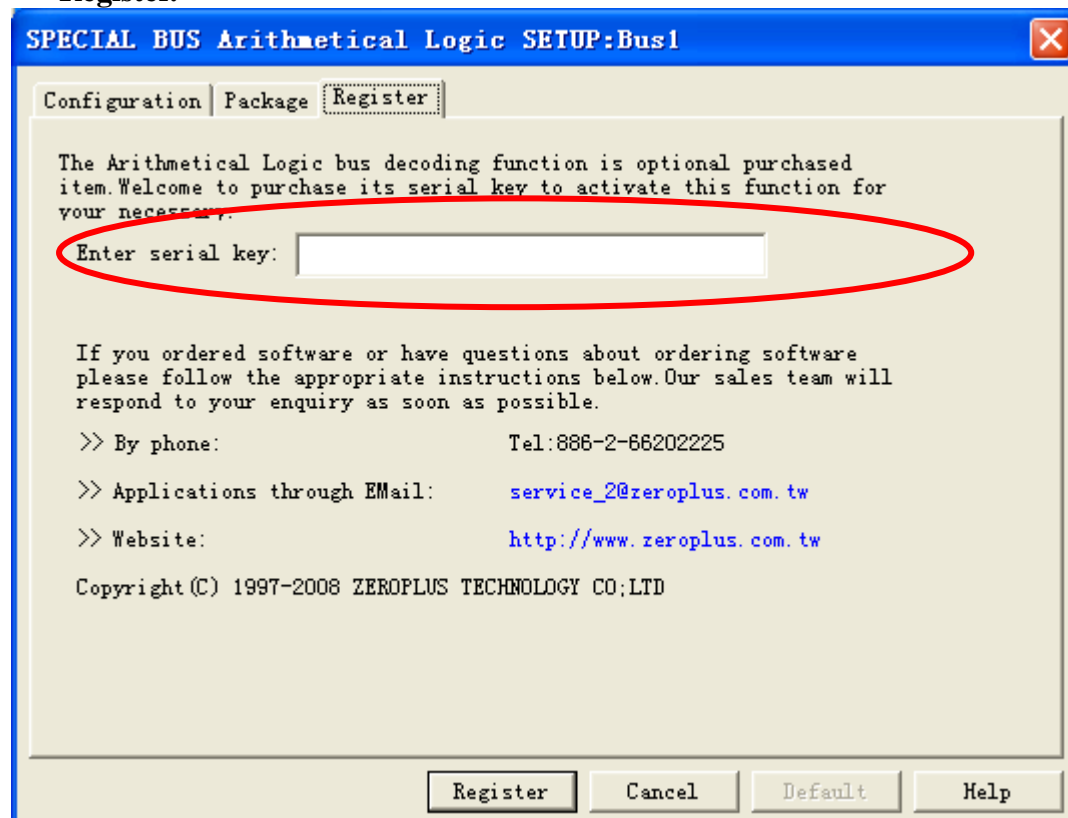
STEP 5.   After completing **Register**, come back to the **Bus property** dialog box, and then click the **parameters configuration** to start the Special Bus Digital Logic setup.
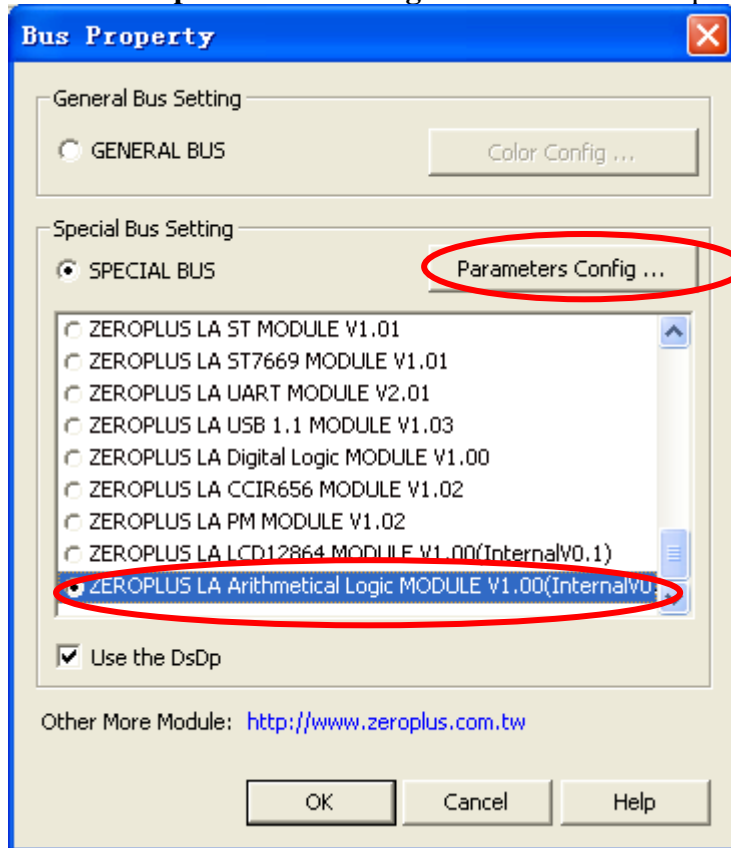
STEP 6.   Arithmetical operation settings, the useable logic elements are Adder, Subtract, Multiplier and Divider.
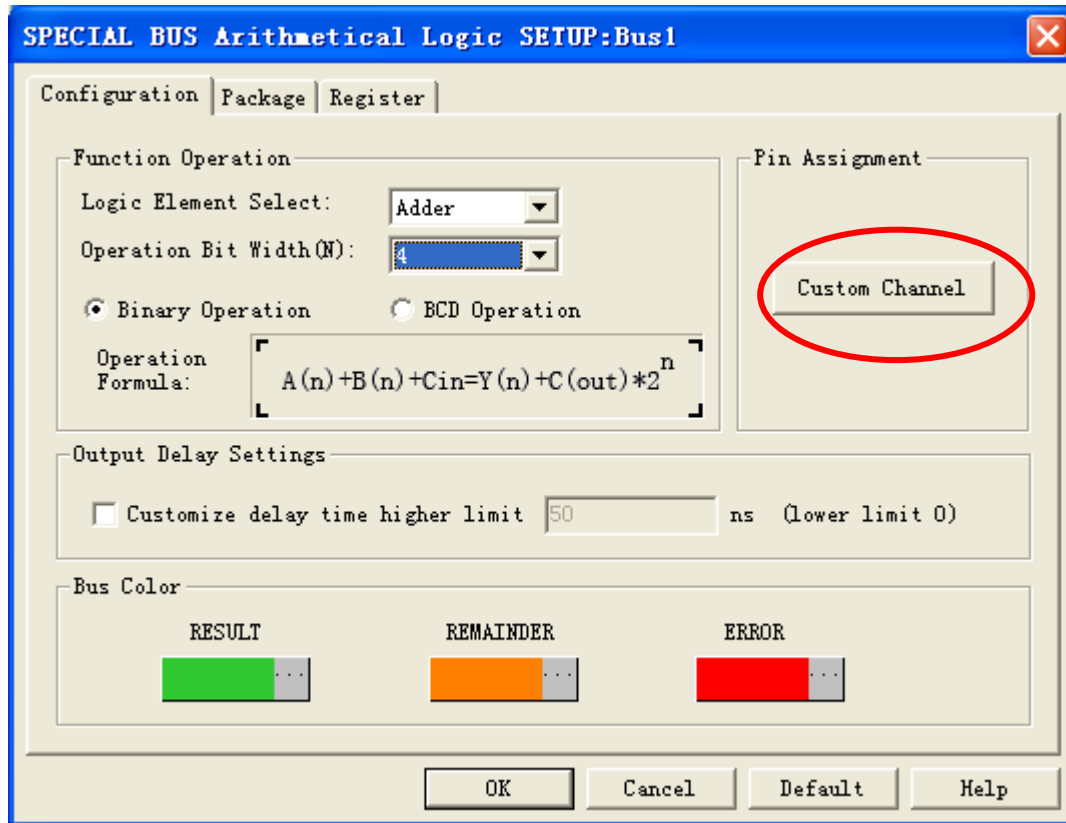
FM-E009A
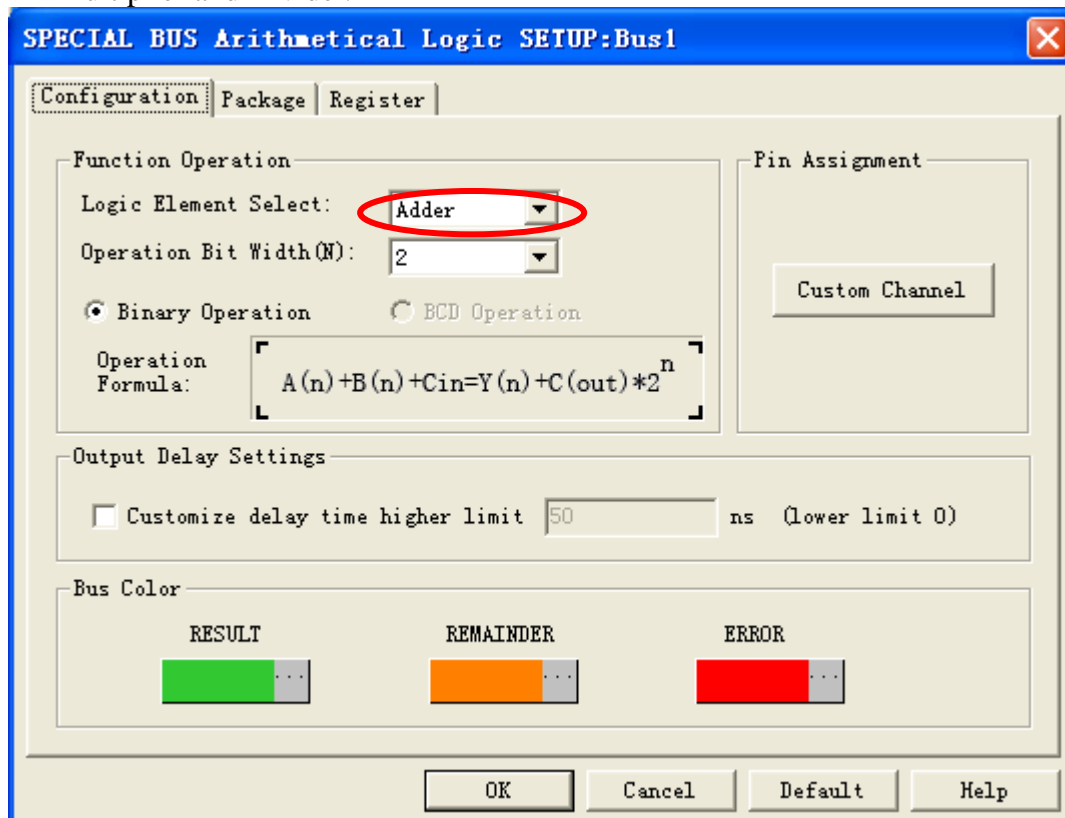
STEP 7. The Operation Bit Width can be selected from the three options, there are: 2 bit, 4 bit and 8 bit.
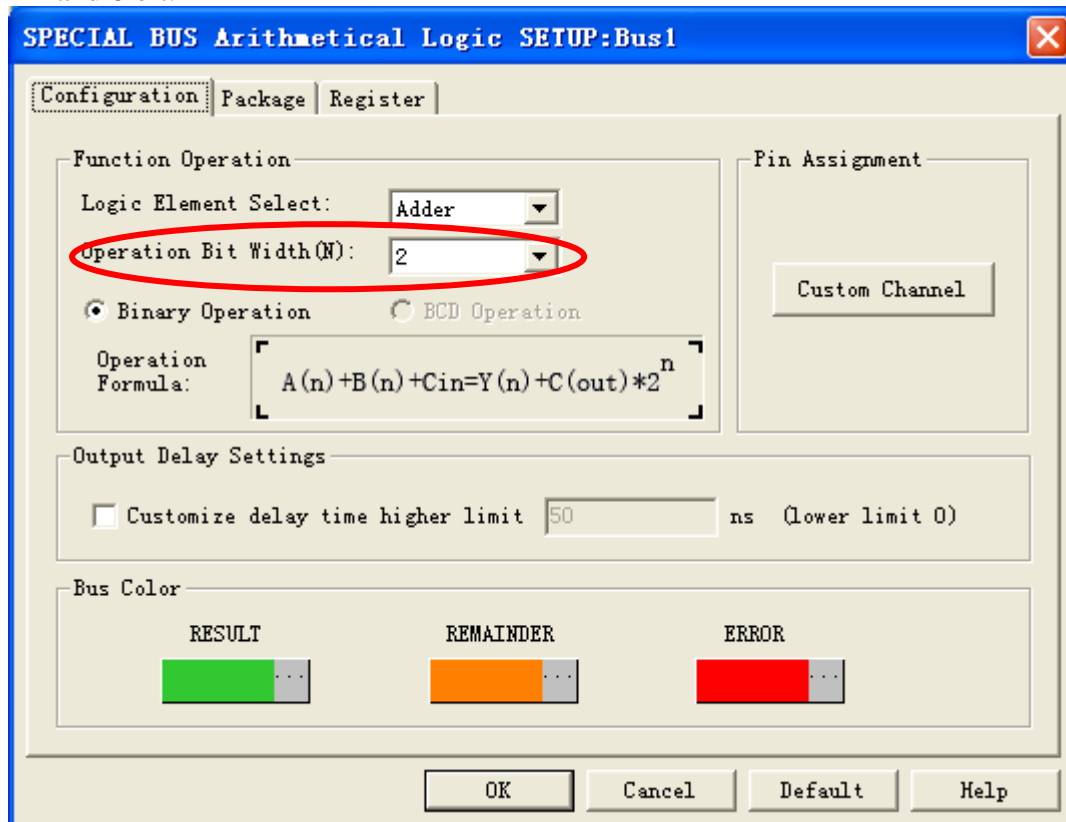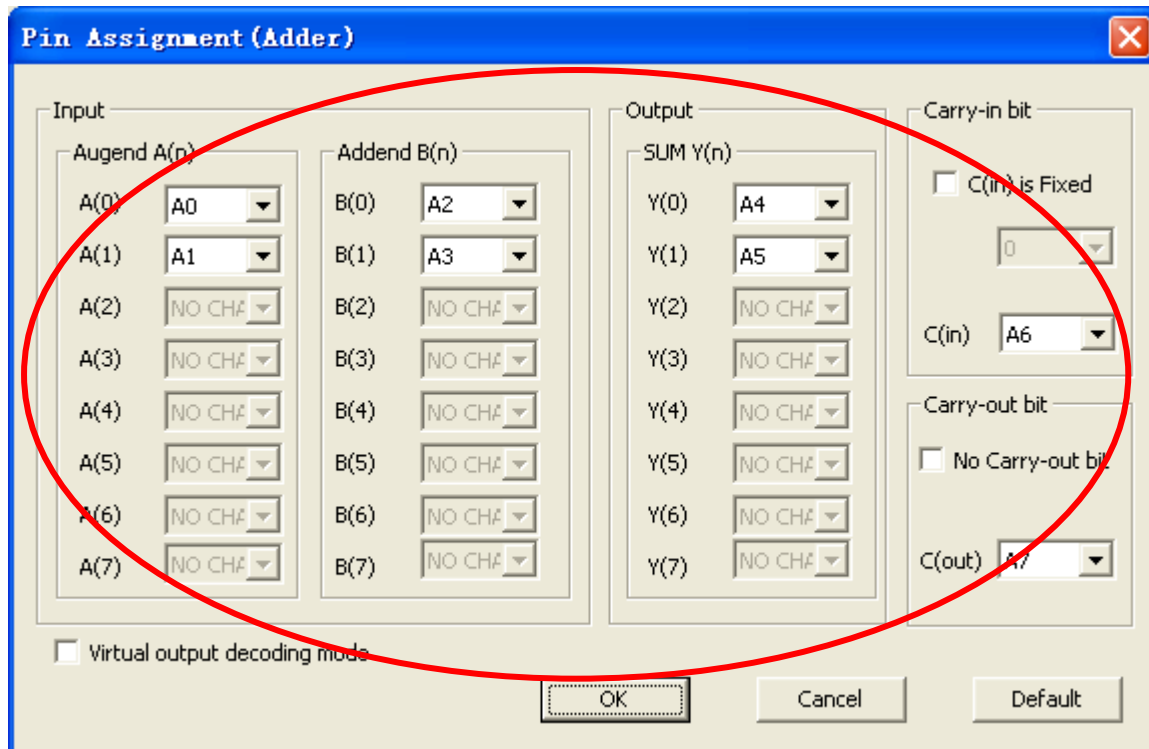

STEP 8. Select the Binary Operation or BCD Operation (The BCD operation is available for the bit width is not less than 4 bit.)

FM-E009A
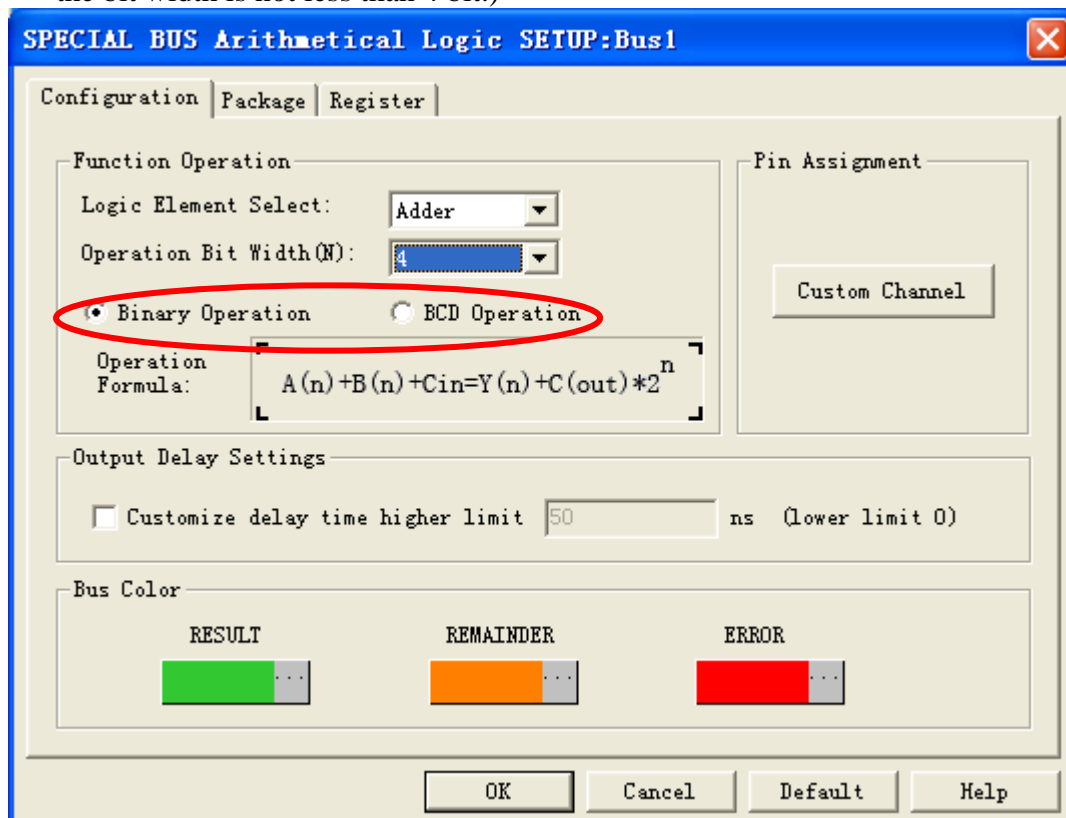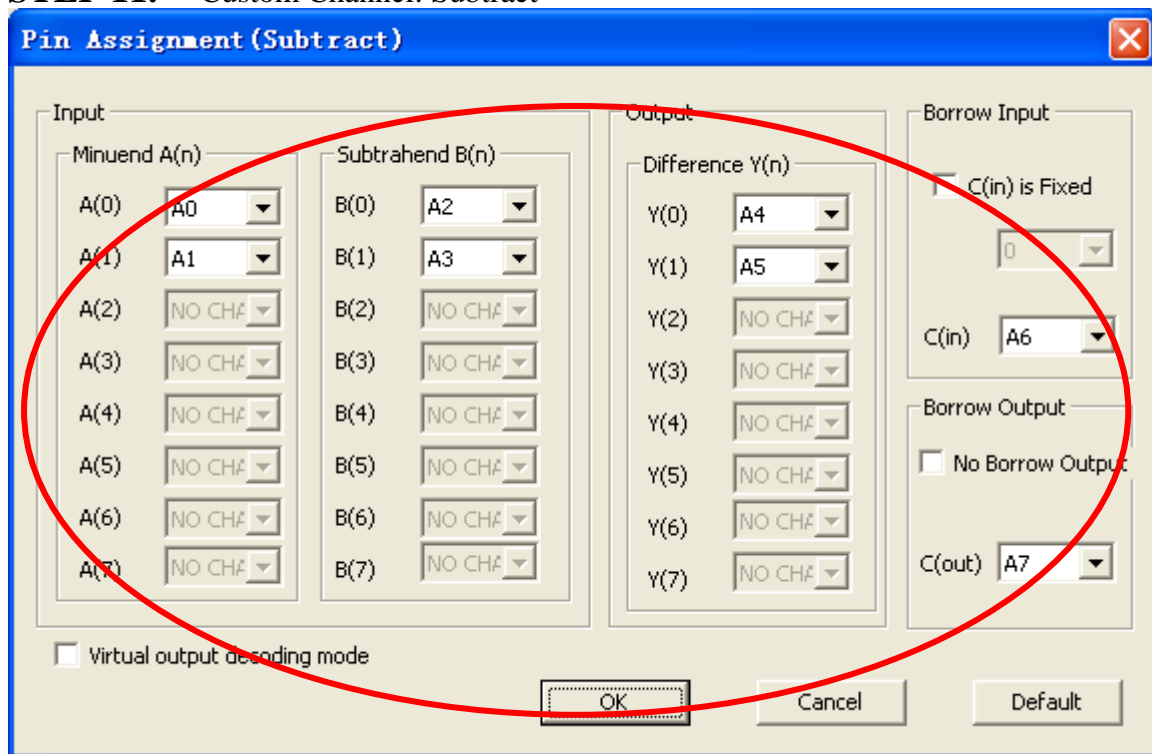
STEP 9. When user selects the different arithmetical elements, the corresponding arithmetical element channel setting will present, and user should set the required channels and the number of the channels.

STEP 10. Custom Channel: Adder

FM-E009A

STEP 11.   Custom Channel: Subtract
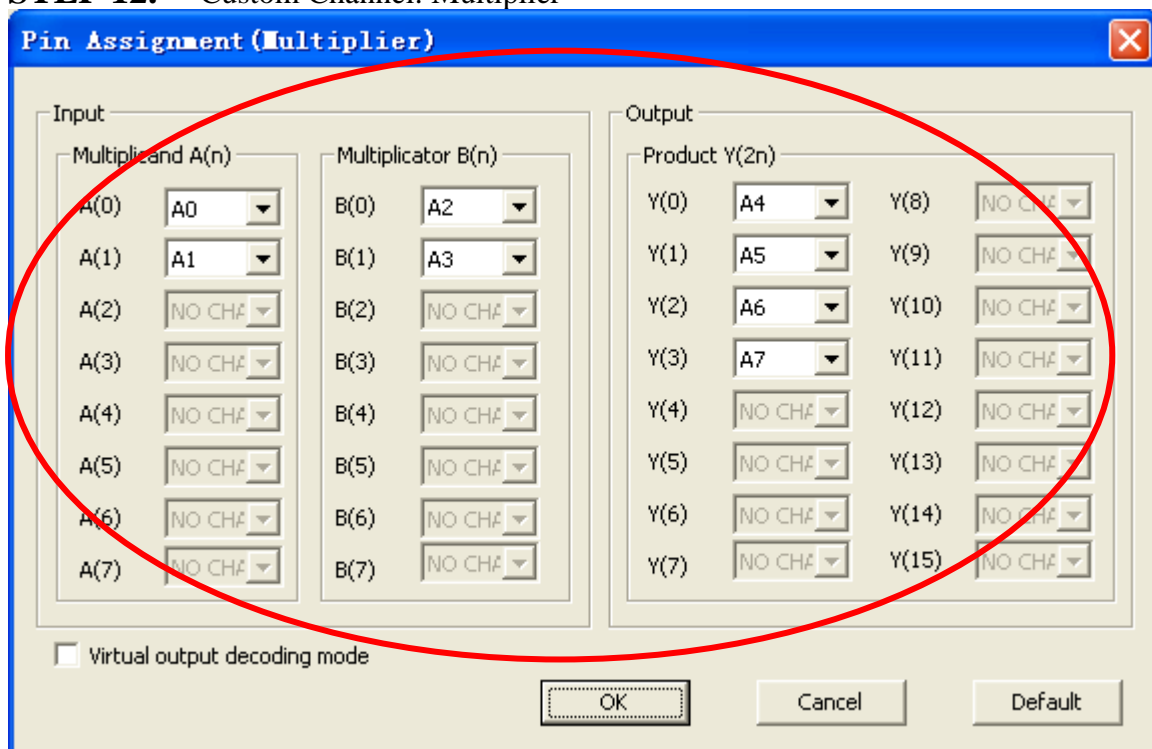
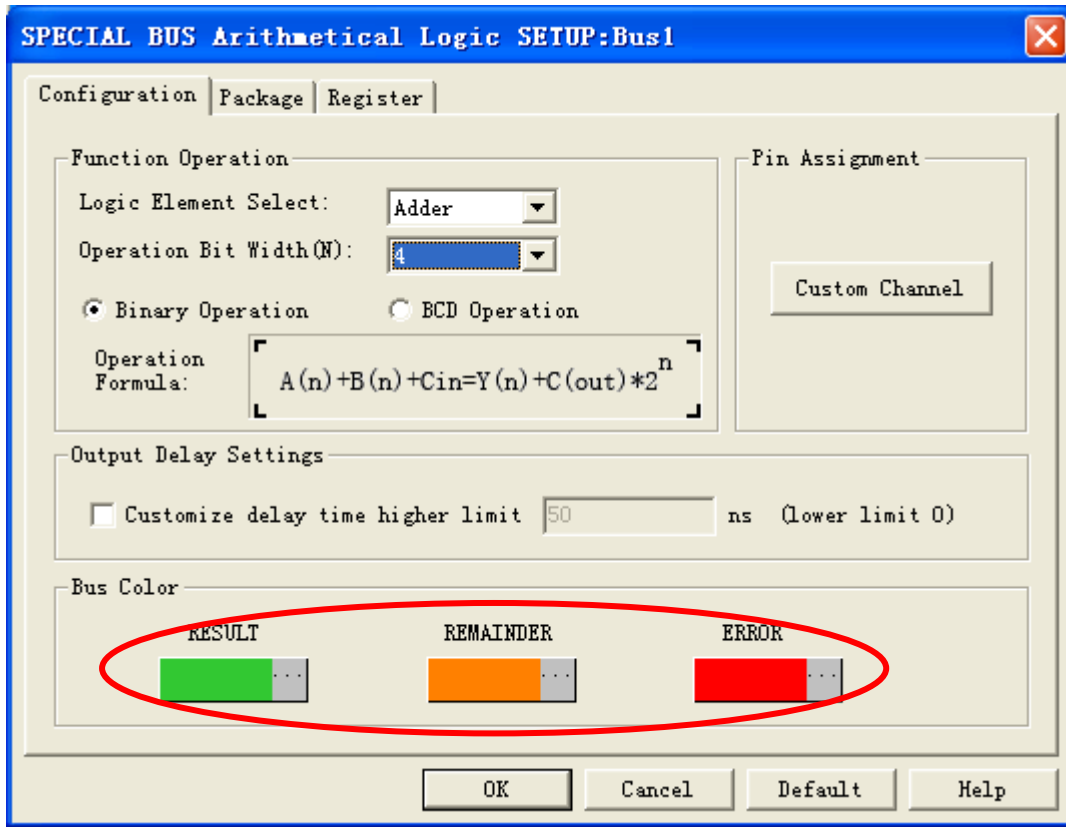**STEP 12.**   Custom Channel: Multiplier

FM-E009A

STEP 13.   Custom Channel: Divider
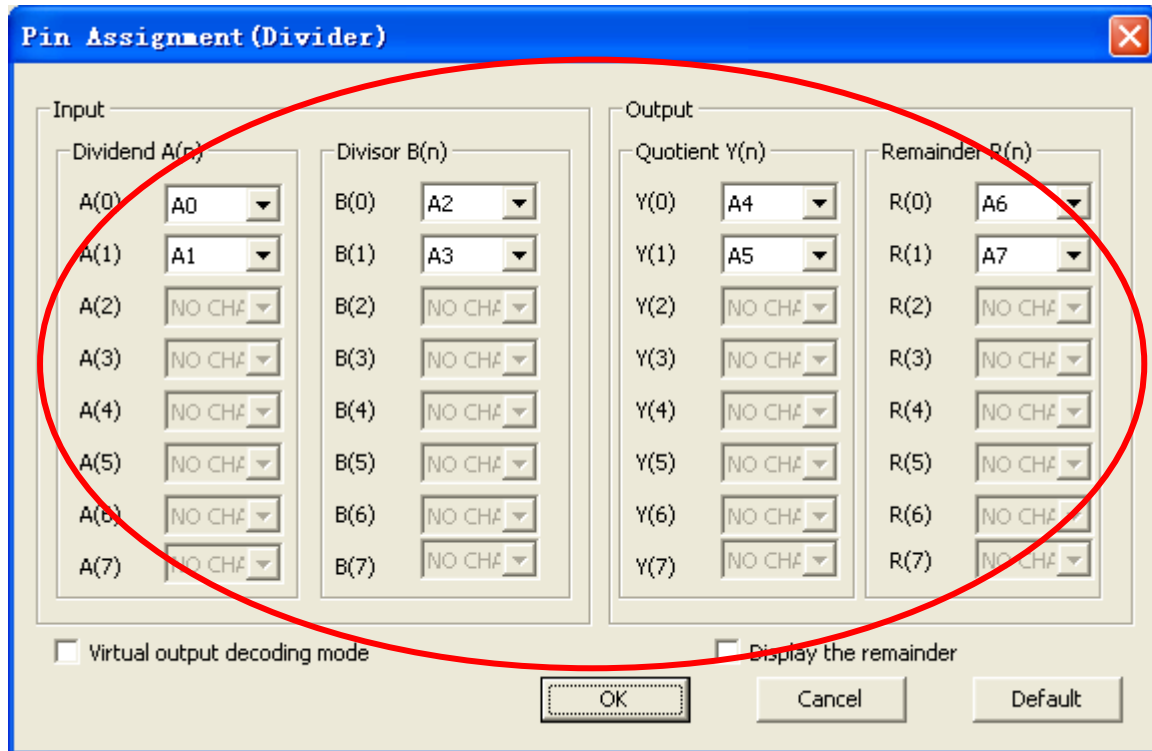
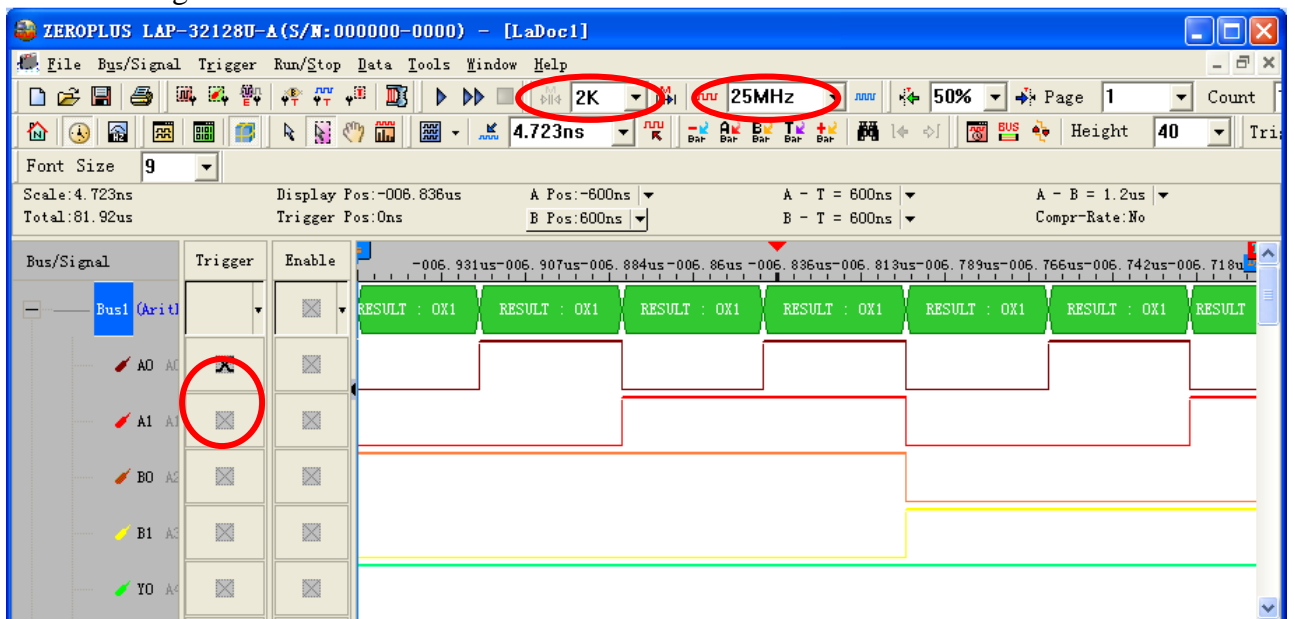**STEP 14.**   Customize the delay time.

FM-E009A
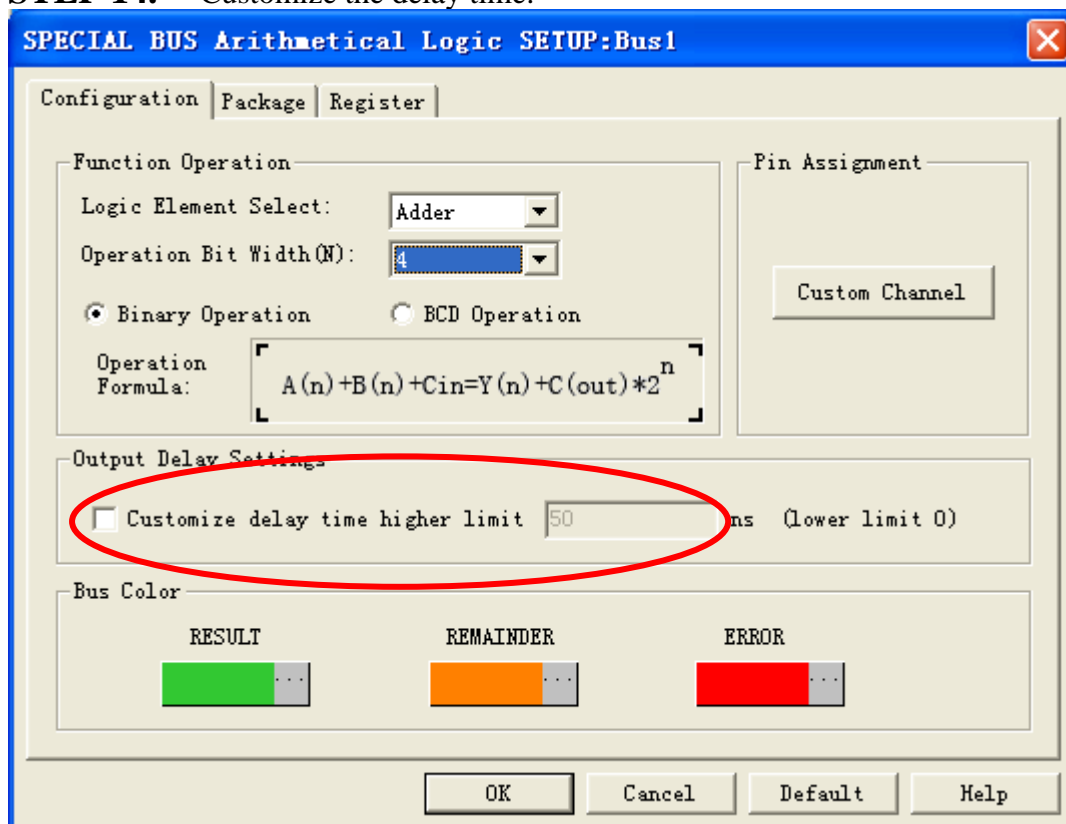
STEP 15. Set the color of the bus

STEP 16. Following pictures show the completion of the Bus decoding and package list. The conditions are set as either edge, the Memory depth is 2K, Sampling frequency is 25MHz

Bus Decoding

FM-E009A

Package List

FM-E009A